

## **ABSTRACT OF THE DISCLOSURE**

A circuit and method for detecting soft errors produced in latches. An exemplary embodiment of a circuit includes a block of concatenated latches, each latch having a comparator, with an output from the final latch comparator representing a parity bit for the latch block. The circuit further includes a latch element to store the block parity bit, and a comparator for the block parity bit and stored parity bit. A latch soft error is detected by monitoring an output from the parity bit comparator, which signals an error when the latch block parity bit changes state.